

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	478234	integrated with circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:04
S2	24868	S1 with (application adj1 specific)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:05
S3	2965	S2 and header	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:06
S4	413	S3 and simulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:07
S5	150	S4 and layout	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:07
S6	150	S5 and (time or timing)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:07
S7	61	S6 and (power with consum\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:07
S8	8	S7 and (hardware with language)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:08
S9	4	S8 and (register\$1 with transfer\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:24

S10	40533	(717/106 or 703/14 or 703/15 or "707"/\$ or 716/18)".class"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:25
S11	3925	S10 and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:25
S12	1068	S10 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:25
S13	1	S12 and S3 and S4 and S5 and S6 and S7 and S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:26
S14	211	S12 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:26
S15	41	S14 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:27
S16	259	S2 and ("717"/\$)".class"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:27
S17	51	S16 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:39
S18	1515	S2 and reus\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:39
S19	34	S2 same reus\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 12:40

S21	55	((hardware adj1 description adj1 language) or HDL) same header\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:45
S22	21	S21 and entity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:10
S23	2	S21 same entity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:11
S24	42486	ASIC or (application with specific with integrated with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 16:08
S25	1947	S24 and (extract\$3 with (layout or simulation or time or timing or power))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 12:59
S26	5762	S24 and (power with consumption)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:14
S27	281	S26 and (layout with area)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:15
S28	38	S27 and (simulat\$3 with time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:15
S29	24	S25 and S28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:16
S31	23	S29 and (RTL or (Register with transfer with level))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:17

S32	22	S29 and ((hardware adj1 description adj1 language) or HDL)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:17
S33	22	S32 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:19
S34	21	S33 and (header or entity)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:21
S35	10	S34 and header	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:21
S36	10	S35 and entity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:26
S37	27	S24 same (re?us\$3 with design)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:47
S38	1	S37 and S36	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:28
S39	57	S24 and (extract\$3 with entity)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:45
S40	10	S39 and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:44
S41	1	S40 and S37	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:37

S42	2	S37 and S39	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:38
S43	0	S24 and (extract\$3 with from with entity)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:44
S44	257	((hardware adj1 description adj1 language) or HDL) same entity	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:46
S45	1	S39 and S44	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:46
S46	609	S24 same (re?us\$3 or customiz\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:48
S47	23	S46 and S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 15:48
S48	940	S24 and (simulation with time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 16:09
S49	34	S48 and (extract\$3 with (simulation with time))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 16:28
S50	63	S48 and (extract\$3 with time)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/16 16:28
S51	48054	ASIC or (application with specific with integrated with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 12:57

S52	2190	S51 and (extract\$3 with (layout or simulation or time or timing or power))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:05
S53	96	S52 and (description with portion\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:00
S54	21	S53 and ((first or head\$2) near2 portion\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:01
S55	6	S54 and ((layout or simulation or time or timing or power) with ((first or head\$2) near2 portion\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:01
S56	245	S51 and (extract\$3 with (layout or (simulat\$3 near3 (time or timing)) or power near3 consum\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:06
S57	41020	(717/106 or 703/14 or 703/15 or "707"/\$ or 716/18)".class	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:06
S58	53	S57 and S56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 13:06
S59	3	S58 and ((description near5 function\$7) same (function\$7 near5 design\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:09
S60	2	("6026228").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:10
S61	63	((hardware adj1 description adj1 language) or HDL) same header\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:55

S62	1	S61 same (entity near2 portion)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:56
S63	3	S61 same (header near3 portion\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:56
S64	48054	ASIC or (application with specific with integrated with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:58
S65	6270	S64 and (power adj1 consumption)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:58
S66	238	S65 and RTL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 16:59
S67	23	S66 and ((stor\$3 or writ\$4 or sav\$3 or record\$3) with (power adj1 consumption))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 17:01
S68	13	S67 and HDL	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 17:01